

**FEE TRANSMITTAL**

Electronic Version v08

Stylesheet Version v08.0

**Title of  
Invention**Method of Manufacturing a Semiconductor Device and a Method for Fixing the  
Semiconductor Device Using Substrate Jig

Application Number :

Date :

First Named Applicant: Kazuo TESHIROGI

Attorney Docket Number: 020334A

**TOTAL FEE AUTHORIZED \$ 856**

Patent fees are subject to annual revisions on or about October 1st of each year.

Filing as large entity

**BASIC FILING FEE**

Fee Description	Fee Code	Amount \$	Fee Paid \$
Utility Filing Fee	1001	770	770
Subtotal For Basic Filing Fees: \$ 770			

**EXTRA CLAIM FEES**

Fee Description	Extra Claim	Fee Code	Amount \$	Fee Paid \$
Total Claims : 6	0	1202	18	0
Independent Claims : 4	1	1201	86	86
Subtotal For Extra Claims Fees: \$ 86				

**AUTHORIZED BILLING INFORMATION****The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:**

Deposit account number: 012340

Access Code \*\*\*\*

Deposit name: Armstrong Kratz Quintos Hanson Brooks LLP

Deposit authorized name: William G. Kratz Jr.

Signature: /WGK/

Date (YYYYMMDD): 2004-02-06